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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: PRITCHARD et al.

Attorney Docket No.:
03-2051/LSI1P240

Application No.: 10/791,337

Examiner: TRINH, Michael Manh

Filed: March 1, 2004

Group: 2822

Title: SPACER-LESS TRANSITOR
INTEGRATION SCHEME FOR HIGH-K GATE
DIELECTRICS AND SMALL GATE-TO-GATE
SPACES APPLICABLE TO SI, SiGe AND
STRAINED SILICON SCHEMES

Confirmation No. 2401

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on June 13, 2006 in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signed: Sue Funchess
Sue Funchess

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement dated May 22, 2006, Applicants hereby elect, without traverse, group III, claims 18-25, to prosecute in the above-identified patent application.

If any fees are due in connection with the filing this Response to Restriction Requirement, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252 (Order No. 03-2051).

Respectfully submitted,

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